PolarFire Documents Summary

This document gives a short excerpt about the documents dealing with the FPGA. This is to help find the correct documentation easily and efficiently.

File Name	Description
A500k_IOSelect_AN.pdf	Gives details of PolarFire I/O cell selection
CoreGPIO_HB.pdf	For CoreGPIO module. Details how to implement the module in various configurations.
Des_constraints_ug.pdf	Gives information on creating a constraints file. There is a tool in Libero that does this graphically, but this may be needed in some cases.
Microsemi_PolarFire_FPGA_Building_Mi-V_Subsystem_Tutorial_TU0775_V5.pdf	This is the tutorial used to build and implement a RISC-V core. It is very helpful and even goes over how to program the FPGA, flash the processor with a program to run, and how to implement debug mode to be used in SoftConsole
Microsemi_PolarFire_FPGA_Evaluation_Kit_User_Guide_UG0747_V5.pdf	Contains general information about the board. This includes information about the FMC HPC connector.
Microsemi_PolarFire_FPGA_User_IO_User_Guide_UG0686_V4.pdf	Contains more detailed information on the PolarFire's I/O. Helpful when mapping I/O to the FMC HPC connections.
SynplifyPro_TMR_instructions.pdf	This was given to us by the sponsor, Randy Wade. The document goes over how to easily implement TMR into your system.
POLARFIRE_EVAL KIT_BOARD_REV-D_SCH_11-09-17_1.pdf	Schematic Datasheet for the PolarFire. Shows schematics and block diagrams for the PolarFire Dev board. This document is helpful for finding how to hook up external connections.