Executive Summary – Samuel Bagley

Contributions

FPGA Design

- Libero Troubleshooting
- Implementing base RISC-V design
- Researched documentation for implementing RISC-V cores
- Researched drivers to properly iterate on provided code
- Wrote C code to initialize and test I2C communication

PCB Design

- Researched sensors to include on PCB in order to test FPGA performance
- Designed schematics to incorporate I2C communication with sensors
- Imported and create footprints and CAD models
- Set up version control for managing PCB design among several team members
- Created table documenting FPGA pin mapping to PCB pins
- Edited entire project schematics to create consistency between team members' work
- Verified BOM and purchase components

Final Report

- Executive Summary
- documentation for I2C implementation
- documentation to assist future teams troubleshoot errors
- additional documentation for C code
- Worked on final presentation

Future Suggestions

The next group to work on this project should focus on getting familiar with the software tools as quickly as possible so that they will better understand how to move forward without getting suddenly stuck when they encounter an error.

We have described in other documents how to get up and running with the software and how to solve some common errors that we had, so I would suggest reading those and also contacting us when the future group has any questions.

Time Table

Week #	Hours spent	Work done
1	9	
ſ	10	Team Mactings (project planning
2	10	Team Meetings/project planning
3	13	Percent petertial sensors
Λ	11	Werk on implementing I2C in VILDL / Leave VILDL
4	11	Work on implementing I2C in VHDL / Learn VHDL
		Setting up Altium version control
~	0	Work on implementing I2C in VIIDL (continue Learning VIIDL
5	ŏ	Soncors into Altium for DCP
		Sensors Into Altum for PCB
G	12	Decign Procesure concertant heartrate concertschematics in Altium
0	15	Manage version control
7	10	Schematia Design
/	12	Schematic Design
ð	15	Altium DCD
0	7	
9	/	Traublashaat Libara
10	20	Troubleshoot Libero
10	20	Implement I2C module into Libero project
11	11	Order Board/Parts
		Help with RISCV Configurations
42		Built arduino test for I2c
12	/	Opdated BOIN with correct part into
40		Ordered/ received parts
13	25	Final Report Documentation
		Project Documentation for continuing work
		Clean up project files
	1.50	leam meetings
Total	159	
Hours:		