

Week 6 (10-Feb to 17-Feb) **Zac Carico**

Goal/Task	% Done	Hours (Act.)
Get Libero Working	0.50	4
Help Team	100	6
Create Altium Top Module	50	2

Hours on task during the week (On track \geq 13 / wk)	
Total hours on task so far this semester (On track \geq 70 hrs)	

Progress made during the week (Log)

(What I did)

- Got the synthesis to work
- Helped James, Michael, and Max with Altium
- Started working on bringing everything together in top module

Difficulties encountered during the week

(What I did not do and why)

- Libero..... again.....

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
GET LIBERO WORKING!	∞	∞
Continue finishing top module and getting everyone's components in order		

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	
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How can we help you achieve your goals?

- Fly people from Microsemi to here to get the program working

Week 6 (10-Feb to 17-Feb) James Thomas

Goal/Task	% Done	Hours (Act.)
Schematic	85%	14
UART Implementation	0%	0

Hours on task during the week (On track \geq 13 / wk)	14
Total hours on task so far this semester (On track \geq 70 hrs)	60

Progress made during the week (Log)

(What I did)

Found a couple of components for the PCB (12-5V converter, HD15 connector)

Worked on schematic. Tried to make it easy for others to use the FMC connector

Difficulties encountered during the week

(What I did not do and why)

- Didn't work on the UART implementation. The Schematic took longer than expected

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
UART Implementation	3/6	13
Finalize Schematic	2/20	2
PCB Layout	2/28	2

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	17
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How can we help you achieve your goals?

- Coordinate PCB Layout

Week 6 (10-Feb to 17-Feb) Max Bakes

Goal/Task	% Done	Hours (Act.)
Finish ADC	100	4
Finish SPI	80-95	10
Board layout meeting	100	2

Hours on task during the week (On track \geq 13 / wk)	16
Total hours on task so far this semester (On track \geq 70 hrs)	63

Progress made during the week (Log)

(What I did)

For example: Resources found (links, manuals), Designs created, Decisions made and corresponding rationale, Photos of prototype progress, etc.

Put the ADC schematic back on the project.

Continued VHDL Spi.

Difficulties encountered during the week

(What I did not do and why)

- none

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Test SPI	2/24	3
Design PCB layout	3/1	20
Assemble PCB	3/15	4

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	13
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How can we help you achieve your goals?

- Coordination.

Week 6 (10-Feb to 17-Feb) Sam Bagley

Goal/Task	% Done	Hours (Act.)
Design Pressure sensor and heartrate sensor schematics in Altium	90	10
Manage version control	100	3

Hours on task during the week (On track \geq 13 / wk)	13
Total hours on task so far this semester (On track \geq 70 hrs)	64

Progress made during the week (Log)

(What I did)

Worked on Schematics. Besides a few passive components, its pretty much done.

Did some planning for circuit layout/connections.

Difficulties encountered during the week

(What I did not do and why)

- Tough to keep Altium work synchronized with git, had to redo some stuff that got messed up. I think we have a semi working system now though.
- Ran into some issues importing passive components and also linking up my schematics with others'

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Create PCB layout and send it for printing.	2/25/20	12
Order components	2/25/20	4

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	16
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How can we help you achieve your goals?

- Finalize and verify Schematics.
- Make sure we use same components when we go to order them.

Week 6 (10-Feb to 17-Feb) Michael Ashford

Goal/Task	% Done	Hours (Act.)
Install Libero	90%	5
Create RISC-V Processor in Libero	90%	5
Build LCD interface	50%	2
Research RISC-V	75%	3

Hours on task during the week (On track ≥ 13 / wk)	15
Total hours on task so far this semester (On track ≥ 70 hrs)	48

Progress made during the week (Log)

(What I did)

Built LCD Screen in Altium

Built some simple projects in Libero

Chatted with Libero tech support about problems

Difficulties encountered during the week

(What I did not do and why)

- Libero License was not installed correctly

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Fix Libero	3/2/20	4
Create RISC-V in TMR	3/2/20	4
LCD Interface	2/24/20	3
Research RISC-V programs	3/2/20	3

Estimated time needed to work on goals for this coming week (typ. 13 hrs)

How can we help you achieve your goals?
