Week 8 (24-Feb to 1-Mar) Sam Bagley

Goal/Task	% Done	Hours (Act.)
Libero Tutorial Project / assisting troubleshooting	?	10
Altium PCB	50	3

Hours on task during the week (On track ≥ 13 / wk)	13
Total hours on task so far this semester (On track ≥ 93 hrs)	89

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Progress made during the week (Log)
(What I did)

Added missing models to Altium, tried to help fix compiler errors. Backseat drove while James did PCB layout.

Decided to do some looking for more libero documentation, found a step by step for implementing a RISC processor.

Difficulties encountered during the week
(What I did not do and why)

- Libero is a very tempermental and complex program. We had some constraint issues.
- Uncovered Sophos Antivirus Conspiracy.

Goals for this coming week
(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Help Finish PCB	3/9	5
Order PCB	3/9	3
Help With Libero stuff		9

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	17

How can we help you achieve your goals?

- Spend as much time as possible this week to catch up.

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Week 8 (24-Feb to 1-Mar) James Thomas

Goal/Task		% Done	Hours (Act.)
Schematic		95	9
Layout	5	2	

Hours on task during the week (On track ≥ 13 / wk)	11
Total hours on task so far this semester (On track ≥ 93 hrs)	81

Progress made during the week (Log)
(What I did)

Finished finding 3d models for pcb layout.

Added (hopefully) final elements to my part of the schematic.

Switched out parts that didn't have specific part numbers.

Played with possible layout for PCB.

Difficulties encountered during the week

(What I did not do and why)

- Only had 3 days to work so wasn't able to finish hours for the week. Didn't work on UART.

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Layout		10
UART		5

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	15

How can we help you achieve your goals?

- We need to schedule a time to do the layout before Thursday if possible

Week 8 (24-Feb to 1-Mar) Zac Carico

Goal/Task	% Done	Hours (Act.)
Getting Libero to work	100	5
Getting a tutorial program to work	50	6
Helping teammates with Altium	N/A	2

Hours on task during the week (On track ≥ 13 / wk)	13
Total hours on task so far this semester (On track ≥ 93 hrs)	

Progress made during the week (Log)
(What I did)

- Found some issues with Libero
- Made meme to express feelings
- Hunted down IT people to make sure that RAM will be installed
- RAM was installed

Goals for this coming week
(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Finish RISC-V Libero tutorial	3/8	3
Implement TMR with core	3/8	8
Help with Layout	3/8	2
Fix next Libero issue	∞	∞+

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	13

How can we help you achieve your goals?

- N/A

Week 8 (24-Feb to 1-Mar) Max Bakes

Goal/Task	% Done	Hours (Act.)
Reviewed schematics		6
Added components, values		3
Hunted IT people down with Zac	100	1

Hours on task during the week (On track ≥ 13 / wk)	10
Total hours on task so far this semester (On track ≥ 93 hrs)	84

Progress made during the week (Log)

(What I did)

Added resistors and capacitors to PCB

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Difficulties encountered during the week

(What I did not do and why)

Minor Altium issues.

Goals for this coming week
(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Finish PCB	3/7	10

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	

How can we help you achieve your goals?

Nothing at the moment

Week 8 (24-Feb to 1-Mar) Michael Ashford

Goal/Task	% Done	Hours (Act.)
Create Processor in Libero	90%	9
Bidirectional Momentary Switch	100%	3
LCD Interface	50%	2

Hours on task during the week (On track ≥ 13 / wk)	14
Total hours on task so far this semester (On track ≥ 93 hrs)	70

Progress made during the week (Log)
(What I did)

- Researched Bidirectional Momentary Paddle Switch, updated PCB schematic with switch and finished the LCD schematic
- Created initial processor based off tutorial that was found
- Worked towards LCD interface module

Difficulties encountered during the week

(What I did not do and why)

Libero issues
Didn't put much time into LCD interface, since the processor design is more important

Goals for this coming week
(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Finish initial processor design, put in TMR	3/16/20	10
LCD Interface	3/9/20	3
Create RISC-V program	3/23/20	3

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	

How can we help you achieve your goals?

I'm doing okay