

# Week 9 (2-Mar to 8-Mar) **Zac Carico**

Goal/Task	% Done	Hours (Act.)
Implemented TMR on current RISC-V core with Sam	100	5
Finished Libero Tutorial and understanding the programming aspect with Sam and Michael	100	3
Helped James and Max route PCB and routed parts of PCB myself	40	3
Fixing Libero due to forgetting to turn Anti-Virus off...	100	2

Hours on task during the week (On track $\geq$ 13 / wk)	13
Total hours on task so far this semester (On track $\geq$ 106 hrs)	

# Progress made during the week (Log)

(What I did)

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- Created coding project to start creating out test program
- Most sensors are fully routed (besides power, ground, and communication connections)
- Implemented TMR with current RISC-V Core

# Difficulties encountered during the week

(What I did not do and why)

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- "*And now, my [team], remember, remember...*" to turn off the anti-virus

# Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Work on completing header file for accelerometer	Sun Mar 16	4
Add in modules needed to work with PCB design	Sun Mar 16	8
Help with routing	Sun Mar 16	2

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	14
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# How can we help you achieve your goals?

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- If someone is working on Libero and Altium, work on the programming aspect.



# Week 9 (2-Mar to 8-Mar) James Thomas

Goal/Task	% Done	Hours (Act.)
Worked on routing	40	13

Hours on task during the week (On track $\geq$ 13 / wk)	13
Total hours on task so far this semester (On track $\geq$ 106 hrs)	

# Progress made during the week (Log)

(What I did)

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- Found correct FMC connector



# Difficulties encountered during the week

(What I did not do and why)

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- Time

# Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Finish Routing		13

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	13
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# How can we help you achieve your goals?

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- If someone is working on Libero and Altium, work on the programming aspect.



# Week 9 (2-Mar to 8-Mar) Michael Ashford

Goal/Task	% Done	Hours (Act.)
Put processor in TMR and ran test program	100%	7
PCB trace routing		2

Hours on task during the week (On track $\geq$ 13 / wk)	9
Total hours on task so far this semester (On track $\geq$ 106 hrs)	79

# Progress made during the week (Log)

(What I did)

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Worked with James on PCB routing

Worked with Zac and Sam on Processor TMR

# Difficulties encountered during the week

(What I did not do and why)

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- Wife has the Flu and an infection on top of the pregnancy
- Limited my time spent on project over the weekend

# Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
LCD Interface	3/16/20	3
Build FMC module in VHDL	3/16/20	4
Add all modules into our Libero project	3/23/20	4
Create program to run on our processor	3/23/20	4

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	15
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## How can we help you achieve your goals?

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- Zac had a great idea to put out a schedule so that we always have someone working in here. This would improve our efficiency, communication between the group, and overall hours spent on the project.



# Week 9 (2-Mar to 8-Mar) Sam Bagley

Goal/Task	% Done	Hours (Act.)
Help Implement TMR	100	5
Troubleshoot Libero		2

Hours on task during the week (On track $\geq$ 13 / wk)	7
Total hours on task so far this semester (On track $\geq$ 106 hrs)	96

# Progress made during the week (Log)

(What I did)

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Helped Zachary troubleshoot and implement TMR and also run code on the FPGA.

We don't have the \$35,000 license for Synplify Premier , so our TMR implementation is slightly limited.

# Difficulties encountered during the week

(What I did not do and why)

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- Didn't contribute much to PCB because I didn't want to mess up James' work while he was gone/ being lazy.

# Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Finish routing		6
Order parts/board		6
Write code to run sensors on FPGA cores		3

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	15
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# How can we help you achieve your goals?

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# Week 9 (2-Mar to 8-Mar) Max Bakes

Goal/Task	% Done	Hours (Act.)
PCB Routing	40	13

Hours on task during the week (On track $\geq$ 13 / wk)	13
Total hours on task so far this semester (On track $\geq$ 106 hrs)	97

# Progress made during the week (Log)

(What I did)

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- Routing, Routing and more Routing

# Difficulties encountered during the week

(What I did not do and why)

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FMC connector,

# Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Finish Routing		4
I	Configuration	

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	
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# How can we help you achieve your goals?

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- If we can switch around the E pins that would be beneficial.

