Week 10 (9-Mar to 15-Mar) Sam Bagley

Goal/Task	% Done	Hours (Act.)
Implement additional i2c into Libero Project	100	20

Hours on task during the week (On track ≥ 13 / wk)	20
Total hours on task so far this semester (On track ≥ 119 hrs)	116

Progress made during the week (Log)

(What I did)

I worked on adding I2c communication into the Libero project. This took a long time to get to properly implement but I managed it.

I was able to figure out how to probe the gpios that I routed the I2C to in order to see if the signal was working properly.

I also added the C code drivers and and test functions into the SoftConsole project in order to control the I2C.

Difficulties encountered during the week
(What I did not do and why)

- Libero gave me a lot of headache when it would mysteriously fail to place & route over and over again with no real error message.

- I discovered that most of these mysterious Libero errors can be fixed by turning the computer off and on again...

Goals for this coming week
(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Build arduino i2c test sketch to talk back and forth via I2C.		3
Help with RISCV Implementations		10
Order Parts/Board		5

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	18

How can we help you achieve your goals?

- Go Team!

BYU
IDAHO

Week 10 (9-Mar to 15-Mar) James Thomas

Goal/Task	% Done	Hours (Act.)
Layout	95%	13

Hours on task during the week (On track ≥ 13 / wk)	13
Total hours on task so far this semester (On track ≥ 119 hrs)	107

Progress made during the week (Log)

(What I did)

Routing all day every day.

BYU
IDAHO

Difficulties encountered during the week
(What I did not do and why)

- Routing issues

Goals for this coming week
(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Finalize Layout		
LVDS UART implementation		

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	

How can we help you achieve your goals?

- Lets get this PCB manufactured

Week 10 (9-Mar to 15-Mar) Max Bakes

Goal/Task	% Done	Hours (Act.)
Routed	100%	15
Program ADC	10%	5
Worked on Final paper	20%	10

Hours on task during the week (On track ≥ 13 / wk)	15/15
Total hours on task so far this semester (On track ≥ 119 hrs)	127

Progress made during the week (Log)
(What I did)

Began the literary wonder that is the Final project paper.

Difficulties encountered during the week
(What I did not do and why)

- Writers block.

Goals for this coming week
(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Final Paper	3/30	12
Code for ADC	3/30	15

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	27

How can we help you achieve your goals?

- Review code when completed

Week 10 (9-Mar to 15-Mar) Michael Ashford

Goal/Task	% Done	Hours (Act.)
Routed	100%	10
Program LCD screen	25%	1
Implement extra external device communication	100%	3

Hours on task during the week (On track ≥ 13 / wk)	14
Total hours on task so far this semester (On track ≥ 119 hrs)	93

Progress made during the week (Log)
(What I did)

Lots of routing (and rerouting)

Difficulties encountered during the week

(What I did not do and why)

- Problems with Git

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Program for LCD	3/30	13

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	13

How can we help you achieve your goals?