Week 12 (23-Mar to 29-Mar) Zac Carico

Goal/Task	% Done	Hours (Act.)
Complete LVDS UART	90% ?	5
Create test program for GPIOs	0%	0
Anything else involving an FPGA	0%	0
Program to potentially control the on/off switch of FPGA and move a camera	50%	10

Hours on task during the week (On track ≥ 13 / wk)	15
Total hours on task so far this semester (On track ≥ 145 hrs)	

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Progress made during the week (Log)

(What I did)

Helped James to potentially working LVDS UART

Started to create program to let others use hardware remotely for next semester

Difficulties encountered during the week
(What I did not do and why)

- The world is burning

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Finish all documentation	Apr 3rd	10
Finish program for next semester	Apr 3rd	10

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	20

How can we help you achieve your goals?

- Pray James and I don't have the virus

Week 12 (23-Mar to 29-Mar) James Thomas

Goal/Task	% Done	Hours (Act.)
LVDS UART implementation	??	10
Started documentation	1%	4

Hours on task during the week (On track ≥ 13 / wk)	14
Total hours on task so far this semester (On track ≥ 145 hrs)	131

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Progress made during the week (Log)

(What I did)

Implemented a makeshift LVDS UART

Added documentation for the PCB schematic sheets and generated some new PCB documentation files (built off what Zac had already done)

Difficulties encountered during the week

(What I did not do and why)

- Covid-19
- Coronavirus
- CV-19

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Documentation	4/6	15

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	15

How can we help you achieve your goals?

- Pray for me

Week 12 (23-Mar to 29-Mar) Sam Bagley

Goal/Task	% Done	Hours (Act.)
Updated BOM with correct part info	100	2
Ordered/ received parts	100	5

Hours on task during the week (On track ≥ 13 / wk)	7
Total hours on task so far this semester (On track ≥ 145 hrs)	134

Progress made during the week (Log)
(What I did)

Went through project and updated/fixed missing data for BOM.

Ordered Parts from Digikey.

Received order today and confirmed all parts were accounted for.

Difficulties encountered during the week
(What I did not do and why)

- Statewide lockdown ruined everything.

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Place, solder components		8
Documentation		10

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	18

How can we help you achieve your goals?

- Give me some specific tasks to work on.

Week 12 (23-Mar to 29-Mar) Michael Ashford

Goal/Task	% Done	Hours (Act.)
Configure Floating Point Version of our RISC-V	75%	5
Work on documentation	10%	3
Benchmark	20%	2

Hours on task during the week (On track ≥ 13 / wk)	10
Total hours on task so far this semester (On track ≥ 145 hrs)	115

Progress made during the week (Log)

(What I did)

Tried to adapt our RISC-V to a version with Floating Point multiplication unit

Worked on some of our documentation for end of project

Tried to benchmark processor using dhrystone code also used at RISC-V competition

Difficulties encountered during the week
(What I did not do and why)

- Adapting current design to FP processing unit

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
End of Project Documentation	4/6/20	15
Benchmarking code	4/6/20	5
FP processor	4/6/20	2

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	22

How can we help you achieve your goals?

- Pray for this mortal world
- Encourage me not to simply take the Pass grade for all my classes

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Week 12 (23-Mar to 29-Mar) Max Bakes

Goal/Task	% Done	Hours (Act.)
ADC Code	100	15
Documentation	5	2

Hours on task during the week (On track ≥ 13 / wk)	17
Total hours on task so far this semester (On track ≥ 145 hrs)	144

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Progress made during the week (Log)
(What I did)

Worked on the ADC code.

Difficulties encountered during the week
(What I did not do and why)

- Programmed in the wrong language VHDL-C

Goals for this coming week

(Ones that move the project forward the most)

Goal/Task	Stop Date (Est.)	Hours (Est.)
Documentation	4/6	20

Estimated time needed to work on goals for this coming week (typ. 13 hrs)	20

How can we help you achieve your goals?

- nothing